



S/N 09/945500

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Leonard Forbes	Examiner:	Ly D. Pham
Serial No.:	09/945500	Group Art Unit:	2818
Filed:	August 30, 2001	Docket:	1303.029US1
Title:	PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUITS WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS		

---

**INFORMATION DISCLOSURE STATEMENT**

Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement.

INFORMATION DISCLOSURE STATEMENT

Serial No :09/945500

Filing Date: August 30, 2001

Title: PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUITS WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS

Page 2

Dkt: 1303.029US1

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

LEONARD FORBES

By his Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

P.O. Box 2938

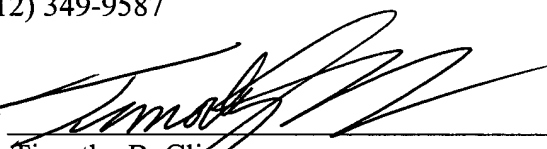
Minneapolis, MN 55402

(612) 349-9587

Date

29 Aug '03

By



Timothy B. Clise

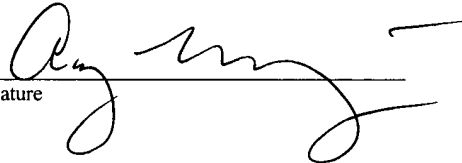
Reg. No. 40,957

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 29th day of August, 2003.

Name

Amy Moriarty

Signature

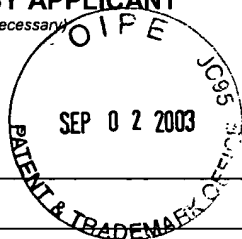


Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Complete if Known

<b>Application Number</b>	09/945500
<b>Filing Date</b>	August 30, 2001
<b>First Named Inventor</b>	Forbes, Leonard
<b>Group Art Unit</b>	2818
<b>Examiner Name</b>	Pham, Ly

Sheet 1 of 1

Attorney Docket No: 1303.029US1

**US PATENT DOCUMENTS**

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
	US-6,210,999	04/03/2001	Gardner, , et al.	438	183	12/04/1998
	US-6,541,280	04/01/2003	Kaushik, , et al.			03/20/2001

**OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		AARIK, JAAN, et al., "Anomalous effect of temperature on atomic layer deposition of titanium oxide", <u>Journal of Crystal Growth</u> , (2000),pp. 531-537	
		AARIK, JAAN, et al., "Texture development in nanocrystalline hafnium dioxide thin films grown by atomic layer deposition", <u>Journal of Crystal Growth</u> , (2000),pp. 105-113	
		FERGUSON, J D., et al., "Atomic layer deposition of Al <sub>2</sub> O <sub>3</sub> and SiO <sub>2</sub> on BN particles using sequential surface reaction", <u>Applied Surface Science</u> , (2000),pp. 280-292	
		KIM, YONG S., et al., "Effect of rapid thermal annealing on the structure and the electrical properties of atomic-layer-deposited Ta <sub>2</sub> O <sub>5</sub> films", <u>Journal of the Korean Physical Society</u> , (December 2000),pp. 975-979	
		KIM, YEONG K., et al., "Novel capacitor technology for high density stand-alone and embedded DRAMs", <u>IEEE</u> , (2000),4 pages	
		KUKLI, KAUPON, "Atomic Layer Deposition of Titanium Oxide from TiI <sub>4</sub> and H <sub>2</sub> O <sub>2</sub> ", <u>Chemical Vapor Deposition</u> , (2000),pp. 303-310	
		KUKLI, KAUPON, et al., "Atomic layer deposition of zirconium oxide from zirconium tetraiodide, water and hydrogen peroxide", <u>Journal of Crystal Growth</u> , (2001),pp. 262-272	
		KUKLI, KAUPON, et al., "Real-time monitoring in atomic layer deposition of TiO <sub>2</sub> from TiI <sub>4</sub> and H <sub>2</sub> O-H <sub>2</sub> O <sub>2</sub> ", <u>American Chemical Society</u> , (2000),pp. 8122-8128	
		LEE, J., "Effect of Polysilicon Gate on the Flatband Voltage Shift and Mobility Degradation for ALD-Al <sub>2</sub> O <sub>3</sub> Gate Dielectric", <u>IEDM</u> , (2000),pp. 645-648	
		PARANJPE, AJIT, et al., "Atomic layer deposition of AlO <sub>x</sub> for thin film head gap application", <u>Journal of the Electrochemical Society</u> , (September 2001),pp. 465-471	
		SMITH, RYAN C., et al., "Chemical vapour deposition of the oxides of titanium, zirconium and hafnium for use as high-k materials in microelectronic devices. A carbon-free precursor for the synthesis of hafnium dioxide", <u>Advanced Materials for Optics and Electronics</u> , (2000),2 pages	

**EXAMINER****DATE CONSIDERED**

Substitute Disclosure Statement Form (PTO-1449)

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached